DATA SHEET



PXI-2022

16 CHANNEL, 250 KSA/S, PXI-H DIGITIZER WITH INTEGRATED DIGITAL I/O AND COUNTER

FEATURES

16-CH differential, 16-bit analog inputs
Simultaneous sampling on every channel, up to 250 kS/s
Programmable gains of x1, x4
Bipolar Analog Input
4-CH TTL Digital I/O
2-CH 32-bit general purpose timer/counter
Digital triggering
Auto-calibration

Driver and SDK support for Windows and Linux, and for third-party applications including Visual Studio, LabVIEW and MATLAB.



OVERVIEW

PXI-2022 is a simultaneous-sampling multi-function DAQ cards to meet a wide range of application requirements. The device can simultaneously sample 16 Analog input channels with differential input configurations in order to achieve maximum noise elimination.

The PXI-2022 features digital triggering, 4-CH programmable digital I/O lines, and 2-CH 32-bit general-purpose timer/counters. The auto-calibration functions adjust the gain and offset to within specified accuracies such that you do not have to adjust trim pots to calibrate the cards.

Software drivers and SDK support are provided for Windows and Linux environments. Wide range of application development environments including Visual Studio, Labview, Matlab and VEE are supported.

Detailed Specifications

ANALOG INPUT

CHANNELS RESOLUTION MAX SAMPLING RATE PROGRAMMABLE GAIN BIPOLAR INPUT RANGES OFFSET ERROR GAIN ERROR OFFSET TEMPERATURE DRIFT INPUT COUPLING OVERVOLTAGE PROTECTION INPUT IMPEDANCE -3 DB SMALL SIGNAL BANDWIDTH (GAIN = 1)

FIFO BUFFER SIZE DNL (GAIN = 1) INL (GAIN = 1) SYSTEM NOISE

CMRR SPURIOUS-FREE DYNAMIC RANGE (SFDR) SIGNAL-TO-NOISE DISTORTION RATIO(SINAD) TOTAL HARMONIC DISTORTION (THD) SIGNAL-TO-NOISE RATION (SNR) DATA TRANSFERS

DIGITAL I/O

NUMBER OF CHANNELS COMPATIBILITY INPUT LOGIC LEVELS

OUTPUT LOGIC LEVELS

OUTPUT DRIVING CAPACITY POWER-ON STATE DATA TRANSFERS

TIMER/COUNTER

NUMBER OF CHANNELS RESOLUTION

16 Channel simultaneous-sampling with differential inputs 16-bit 250 kS/s 1, 4 ±10 V, ±2.5 V 0.6 mV (typical) 0.05% of input 0.1 mV/°C (typical) DC Power on: Continuous ±30 V, Power off: Continuous ±30V 1 GΩ / 100 pF Gain = 1: 1 MHz Gain = 4: 700 kHz 8 kSa (16 kB) ±0.8 LSB ±1.5 LSB (typical), ±3.0 LSB (MAX) Gain = 1: 0.5 mVRMS Gain = 4: 0.2 mVRMS 80 dB (all ranges) 87 dB 82 dB -85 dB 84 dB Polling, scatter-gather DMA

4 input/output TTL/CMOS Input Iow voltage: 0.8 V (max) Input high voltage: 2.0 V (min) Output Iow voltage: 0.4 V (max) Output high voltage: 2.8 V (min) ±24 mA Input, pull-Iow with 10 kΩ resistor Polling mode

2 32-bit

Specifications contained within this document are subject to change without notice

Detailed Specifications

AUTO CALIBRATION

ONBOARD REFERENCE RECOMMENDED WARM-UP TIME TEMPERATURE DRIFT STABILITY +5.000 V 15 minutes ±3 ppm/°C 50 ppm/1000 Hrs

GENERAL SPECIFICATIONS

PXI BUS TYPE MAXIMUM THROUGHPUT CONNECTOR OPERATING ENVIRONMENT

STORAGE ENVIRONMENT

PXI Hybrid Compatible 132 MB/s ACL-10568-1, 68-pin VHDCI-type female 0 to 55°C 10 to 90% non-condensing -20 to 80°C 5 to 95% non-condensing

Notes:

1) All specifications are typical unless otherwise stated as a minimum or maximum.

2) For current detailed specification please refer to the on-line manual at www.vtiinstruments.com.

3) All specifications subject to change without notice.

4) All specifications assume within 24 hours and 5°C of self-calibration temperature unless otherwise specified

5) Distributed product. These products are manufactured and supported by other leading vendors.

Ordering Information

PXI-2022	16 AI, 4 DIO, 2 Counter, Multifunction, PXI-H Module
RELATED PRODUCTS	
EMX-4350	4-Channel, 625k Sa/s Smart Dynamic Signal Analyzer
EMX-4250	16-Channel, 200k Sa/s Smart Dynamic Signal Analyzer
CMX09	9-slot, 3U PXI Express Chassis
CMX18	18-slot 3U PXI Express Chassis

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